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APPLICANTS

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** CONTINUING DATA ***** none *JK*** FOREIGN APPLICATIONS ***** none *JK*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 08/14/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature: <i>J. Kennedy</i> Initials: <i>JK</i>	TAIWAN	10	10	2

ADDRESS

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 NORTH AMERICA INTERNATIONAL PATENT OFFICE (NAIPC)
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 TITLE
 PROCESS FOR PLANARIZING ARRAY TOP OXIDE IN VERTICAL MOSFET DRAM ARRAYS

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